SECTION 5.5

POTENTIAL INSTRUCTION

if00 [pras [-IfmtXXYZ-[ox-x|xo-x], -IfmtXXYZ-[ox-x|xo-x]]]: instruction

|| ifmt vs ifmt | ifmt vs stream | stream vs ifmt | stream vs stream

|| pras: exists between | qran: not exist between

|| Only for stream(s) and free (not for typed)

|| -- ++ []

if00 [qran [-IfmtXXYZ-[ox-x|xo-x], -IfmtXXYZ-[ox-x|xo-x]]]: 1^^1

instruction w

instruction x

instruction y

instruction z

1==1

|| SCIL at L1

|| Introductory boundary

|| Terminal boundary

uf00 [qran [-IfmtXXYZ-[ox-x|xo-x], -IfmtXXYZ-[ox-x|xo-x]]]: 1^^1

instruction w

instruction x

instruction y

instruction z

1==1